

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Atty. Docket No.: 003921.00191

Charles Pfeil.

Serial No.: 10/780,902

Group Art Unit: 2825

Filed: February 19, 2004

Examiner: Naum B. Levin

For: PARALLEL ELECTRONIC DESIGN AUTOMATION; DISTRIBUTED
SIMULTANEOUS EDITING

**MISCELLANEOUS PAPER CONCERNING DECEMBER 8, 2006 AND
AUGUST 2, 2006 INFORMATION DISCLOSURE STATEMENTS**

U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Amendment
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

Applicants herein provide the following additional information regarding the below-listed references from their August 2, 2006 and December 8, 2006 Information Disclosure Statements:

Paragraph [00032] of JP07-175842 (cited in 12-08-06 IDS) corresponds to col. 6, lines 3-22 of EP0750267.

JP09-288690 (cited in 8-2-06 IDS) teaches editing a design at a design terminal 1, sending an update requesting message together with the updated design from the design terminal 1 to a shared-design-data management apparatus 2, incorporating the design change into design data at the shared-design-data management apparatus 2, and sending the updated design data to another relevant design terminal 1 for the purpose of inquiring whether such an update is acceptable.

JP06-203108 (cited in 12-08-06 IDS) teaches a first operator operating a first operation terminal 4 to edit a design, which can be observed by a second operator watching the screen of a second display terminal 10.

JP09-62726 (cited in 12-08-06 IDS) teaches reflecting at the main system the design information updated by a sub system and transferring the updated information other sub systems.

JP09-212530 (cited in 8-2-06 IDS) teaches setting modification levels to each object such as a “drawing” shown in Fig. 5(A). The modification levels may include a level for text modification, a level for figure attribute modification, a level for label modification, and a level for figure modification. Requests to modify at the same level are not allowed, thereby preventing conflicting modifications from being made.

JP05-74942 (cited in 12-08-06 IDS) describes that multiple designers can separately and simultaneously edit the same cell data by designating an area of a layout pattern, by separating the designated area for editing, and by merging the designated and separated area after editing by the designer.

Respectfully submitted,

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